7.0 Appendices

Appendix A: I/O Processor Ports

(a) 68HC711D3 Built-in Ports

Name	Pin	I/O	Function
PA0	30	I	IR sensor input
PA1	29	I	Remote keypad input (via panel jack J1)
PA2	28	I	Built-in keypad input
PA3	27	O	Enable switcher output into UART (active high)
PA4	26	O	Timer interrupt to 68000
PA5	25	O	Chip select for Real Time Clock (active high)
PA6	24	O	Serial clock for Real Time Clock
PA7	23	I/O	Serial Data (bidirectional) for Real Time Clock
PB0	39	O	68000 reset (active high)
PB1	38	O	NMI disable (active high)
PB2	37	O	Power enable (active high)
PB3	36	O	Heater standby (active low)
PB4	35	O	LED register load pulse (active high)
PB5	34	O	Input buffer enable (active low)
PB6	33	O	Write pulse to 68000 latch (active low)
PB7	32	O	Read pulse to 68000 latch (active low)
PC0	3	I/O	Parallel Data I/O (Bit 0) LSb
PC1	4	I/O	Parallel Data I/O (Bit 1)
PC2	5	I/O	Parallel Data I/O (Bit 2)
PC3	6	I/O	Parallel Data I/O (Bit 3)
PC4	7	I/O	Parallel Data I/O (Bit 4)
PC5	8	I/O	Parallel Data I/O (Bit 5)
PC6	9	I/O	Parallel Data I/O (Bit 6)
PC7	10	I/O	Parallel Data I/O (Bit 7) MSb
PD0	16	I	RS-232 input (receive)
PD1	17	O	RS-232 output (transmit)
PD2	18	O	DAC bus serial data output
PD3	19	O	DAC bus serial clock output
PD4	20	O	I2C serial clock output (inverted)
PD5	21	O	I2C serial data output (inverted)
PD6	13	I	I2C serial data input (non-inverted)
PD7	12	---	unused

(b) Port C Expansion

U7, LED Register:

Bit	Pin	Description
PC0	2	Power-on (green)
PC1	5	Standby (yellow)
PC2	6	Error (red)
PC3	9	LVPS fail (red)
PC4	12	EHT fail (red)
PC5	15	Horiz. fail (red)
PC6	16	Vert. fail (red)
PC7	19	'C' (red)

U19, Input Buffer:

Bit	Pin	Description
PC0	2	high if remote plugged into J1
PC1	4	EHT-INHIBIT (active high)
PC2	6	EHT-FAIL (active high)
PC3	8	low if +5V power rail < 4.6V
PC4	11	case cover interlock
PC5	13	not used (tied low)
PC6	15	high if U18 holds unread data
PC7	17	low if U20 holds unread data